

Docket No. 214398US2SRD

jc997 U.S. PTO
09/961372
09/25/01


IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF: Hideto FURUYAMA
SERIAL NO: NEW APPLICATION
FILED: HEREWITH
FOR: OPTICAL INTERCONNECTION CIRCUIT BOARD AND MANUFACTURING METHOD THEREOF

GAU:
EXAMINER:

INFORMATION DISCLOSURE/RELATED CASE STATEMENT UNDER 37 CFR 1.97

ASSISTANT COMMISSIONER FOR PATENTS
WASHINGTON, D.C. 20231

SIR:

Applicant(s) wish to disclose the following information.

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1/24/02

REFERENCES

The applicant(s) wish to make of record the references listed on the attached form PTO-1449. Copies of the listed references are attached, where required, as are either statements of relevancy or any readily available English translations of pertinent portions of any non-English language references.

A check is attached in the amount required under 37 CFR §1.17(p).

RELATED CASES

Attached is a list of applicant's pending application(s) or issued patent(s) which may be related to the present application. A copy of the patent(s), together with a copy of the claims and drawings of the pending application(s) is attached along with PTO 1449.

A check is attached in the amount required under 37 CFR §1.17(p).

CERTIFICATION

Each item of information contained in this information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this statement.

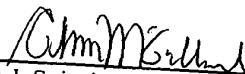
No item of information contained in this information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application or, to the knowledge of the undersigned, having made reasonable inquiry, was known to any individual designated in 37 CFR §1.56(c) more than three months prior to the filing of this statement.

DEPOSIT ACCOUNT

Please charge any additional fees for the papers being filed herewith and for which no check is enclosed herewith, or credit any overpayment to deposit account number 15-0030. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

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IN RE APPLICATION OF: Hideto FURUYAMA

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FOR: OPTICAL INTERCONNECTION CIRCUIT BOARD AND MANUFACTURING
METHOD THEREOF**STATEMENT OF RELEVANCY****Reference AO (2000-114581) on Form PTO-1449:**

This reference shows a optical interconnection board with an embedded optical-semiconductor devices and some vertical optical couplers. (Fig. 28, 29, 30, 31)

Reference AW on Form PTO-1449:

This reference shows a kind of optical interconnection circuit board structure, using a combination of optical waveguide board and 45 degree micro mirrors board. (Figure 1)

Reference AX on Form PTO-1449:

This reference shows a buried optical mirrors by using etched S; substrate, but it has a excess loss of guided optical wave by optical mode change at bended point of optical waveguides.

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LIST OF RELATED CASES

<u>Docket Number</u>	<u>Serial or Patent No.</u>	<u>Filing or Issue Date</u>	<u>Status or Patentee</u>
192718US2 RD	09/603,896	06/26/00	PENDING